UNIVERSITY OF KERALA <u>TIME TABLE FOR THIRD SEMESTER B.TECH DEGREE</u> <u>EXAMINATION</u> <u>MARCH- APRIL 2018</u> (2013 SCHEME)

(For Regular candidates of 2016 Admission & Improvement/ Supplementary Candidates of 2015 Admission of University College of Engineering, Karyavattom)

DAY	DATE	Тіме	SUBJECT
TUESDAY	27/03/2018	9:30 am to 12:30 pm	Engineering Mathematics II (FRT)
TUESDAY	03/04/2018	9:30 am to 12:30 pm	Humanities (FR) Signals & Systems (T)
THURSDAY	05/04/2018	9:30 am to 12:30 pm	Discrete Structures (RF) Network Analysis (T)
TUESDAY	10/04/2018	9:30 am to 12:30 pm	Electronic Devices and Circuits (FR) Analog Communications (T)
THURSDAY	12/04/2018	9:30 am to 12:30 pm	Digital System Design (FR) Electronic Circuits (T)
TUESDAY	17/04/2018	9:30 am to 12:30 pm	Data Structures and - Algorithms (FR) Digital Electronics (T)

University Buildings Thiruvananthapuram Date :14/03/2018

CONTROLLER OF EXAMINATIONS

1